SFX II PXIe C4/FXT

Multifunctional JTAG/Boundary Scan Controller

- high-performance controller for embedded test and programming
- outstanding flexibility through multifunctional I/O channels and FPGA instrumentation for mixed-signal and high-speed test
- high modularity and scalability through configurable TAP interface cards and additional I/O modules
- compact controller including 4 TAPs
- requires only one slot and can be controlled via a PXI Express Rack

GOPEL electronic

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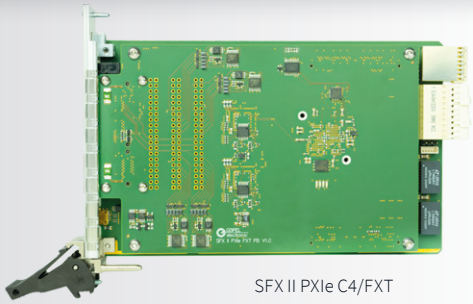
Technical specifications

SFX II PXIe C4/FXT - JTAG/Boundary Scan Controller

Parameters	
number of TAP slots	4 x SFX II TEM (fixed integrated)
maximum TCK frequency	100 MHz (adjustable via software)
parallel I/O channels	32 mixed-signal channels combined with FPGA, individually configurable as Input, Output, Tri-State, via software programmable VIO 0.9 - 3.6 V (4 groups with 8 I/O)
maximum number of I/O modules	over extension interface
integrated technologies	SPACE™, HYSCAN™, ADYCS™, ChipVORX®, FastScale™

Embedded test

- support of latest technologies such as Processor Emulation Test, FPGA Assisted Test and Embedded Diagnostics Test
- synchronization with multifunctional I/O channels and ChipVORX® FPGA instruments



SFX II PXIe C4/FXT

Performance Level

- use of modern FPGA
- simultaneous operation with up to 100 MHz on all TAPs
- operation with up to 100 MHz on I/O channels
- support for gang operations

Embedded programming

- high-speed programming of Flash components like NAND, NOR, SPI, I²C, eMMC etc. (also via I/O)
- universal programming of microcontrollers
- FPGA/PLD programming

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Flexible controller for embedded test and programming

Software

- integrated into Embedded JTAG Solutions platform SYSTEM CASCON™
- Plug and Play integration in 3rd party systems
- open mix of test and programming procedures in one software environment

Expandability

- support of I/O modules
- scalable number of one to four TAPs
- interfaces for additional debug and control components

Adaptability

- TAP signals can be transmitted on the up to four meter long differential lines to the target without TCK reduction
- software-parameterized I/O (slew rate, impedance, termination)
- via software selectable protocols (JTAG, DAP, COP, SWD, UART, BDM, SBW)

Made in Germany

GÖPEL electronic

Enjoy Testing

ISO 9001 certified

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